

FIG. 1

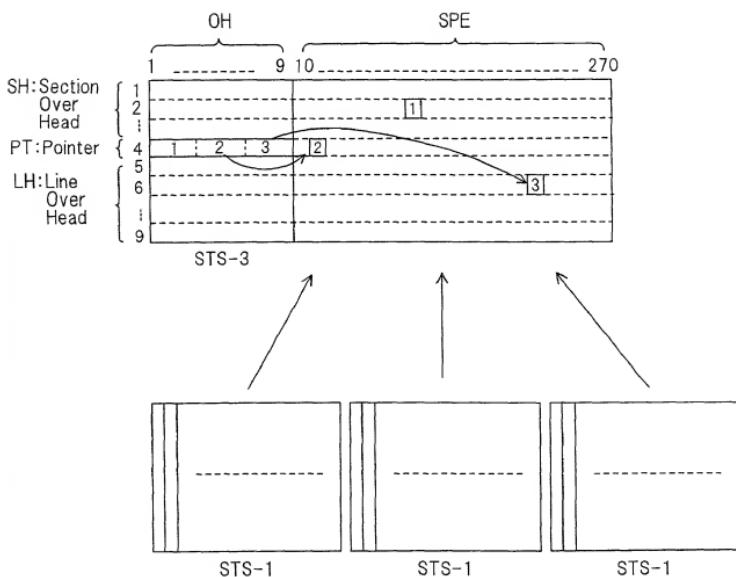


FIG. 2

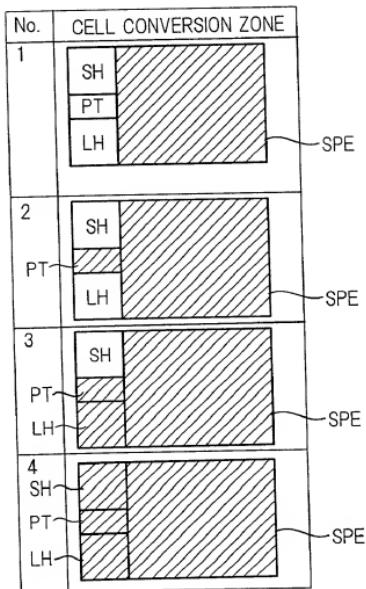


FIG. 3

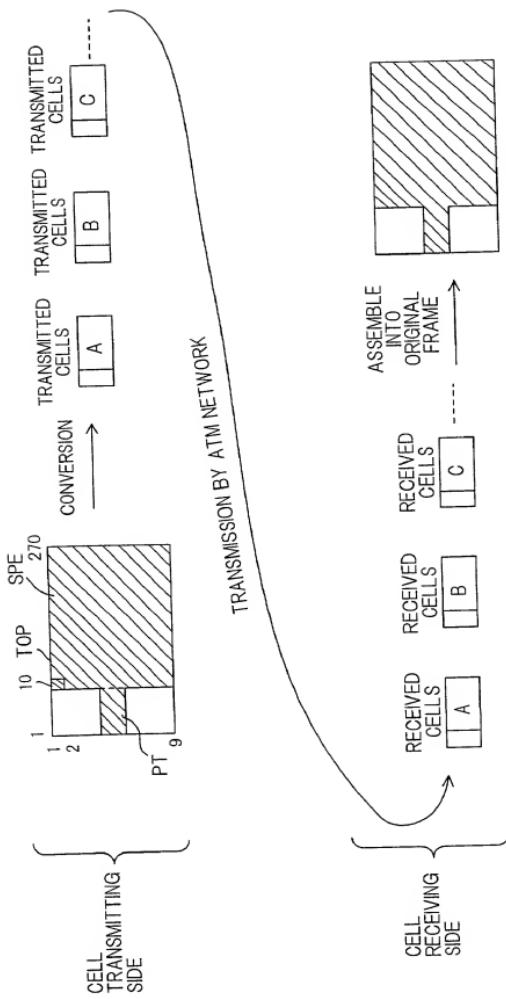


FIG. 4

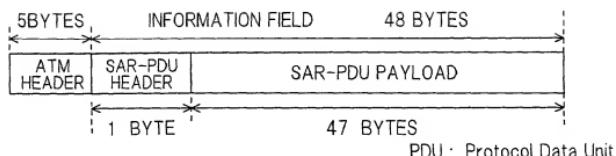
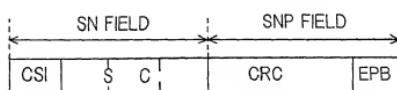


FIG. 5



CSI : Convergence Sublayer Identifier

SC : Sequence Count

CRC : Cyclic Redundancy Check

EPB : Even Parity bit

FIG. 6

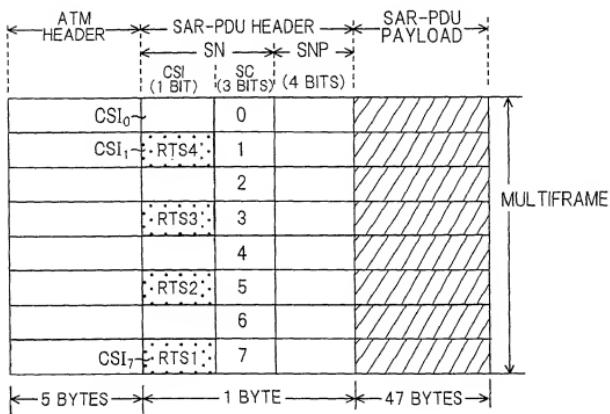


FIG. 7A

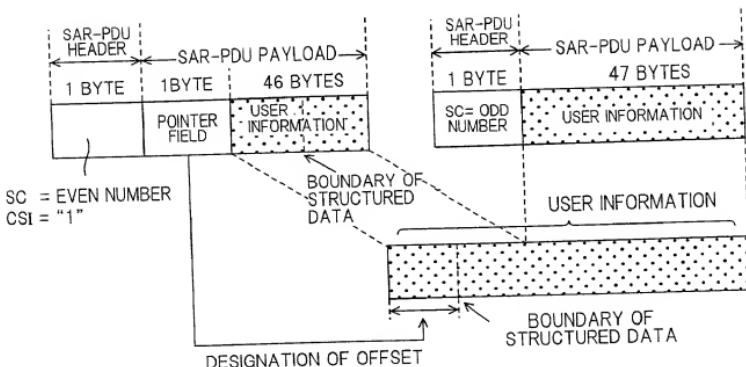
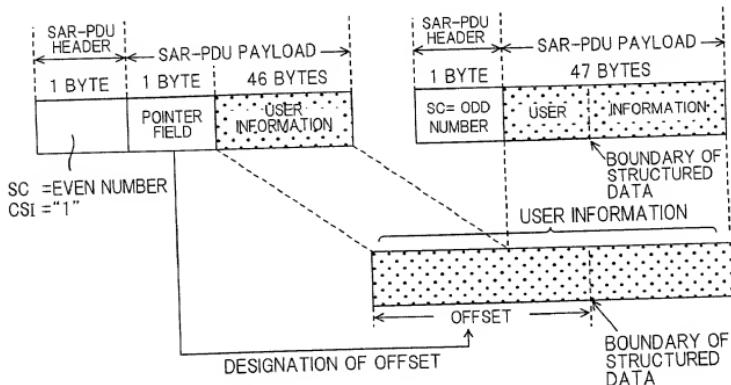


FIG. 7B



*F/G. 8*

图 8 为某通信系统的框图。

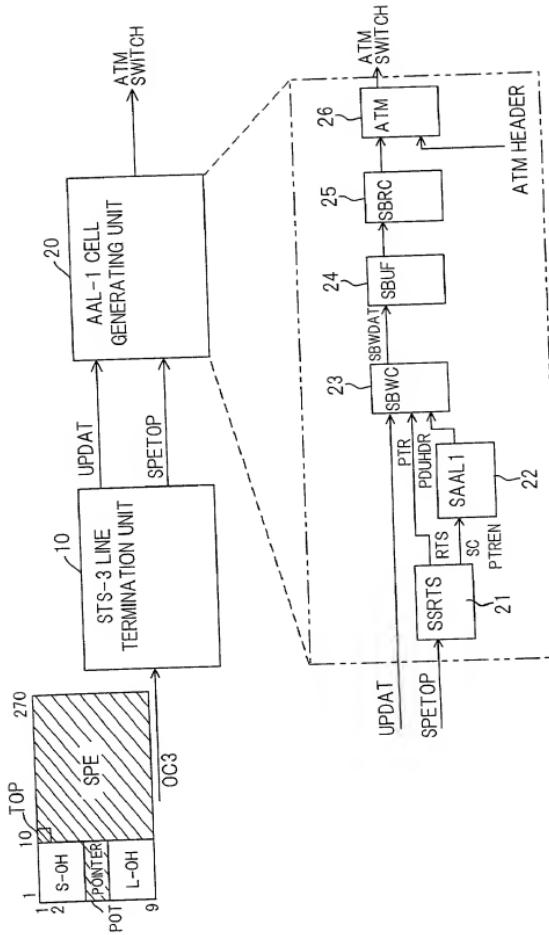
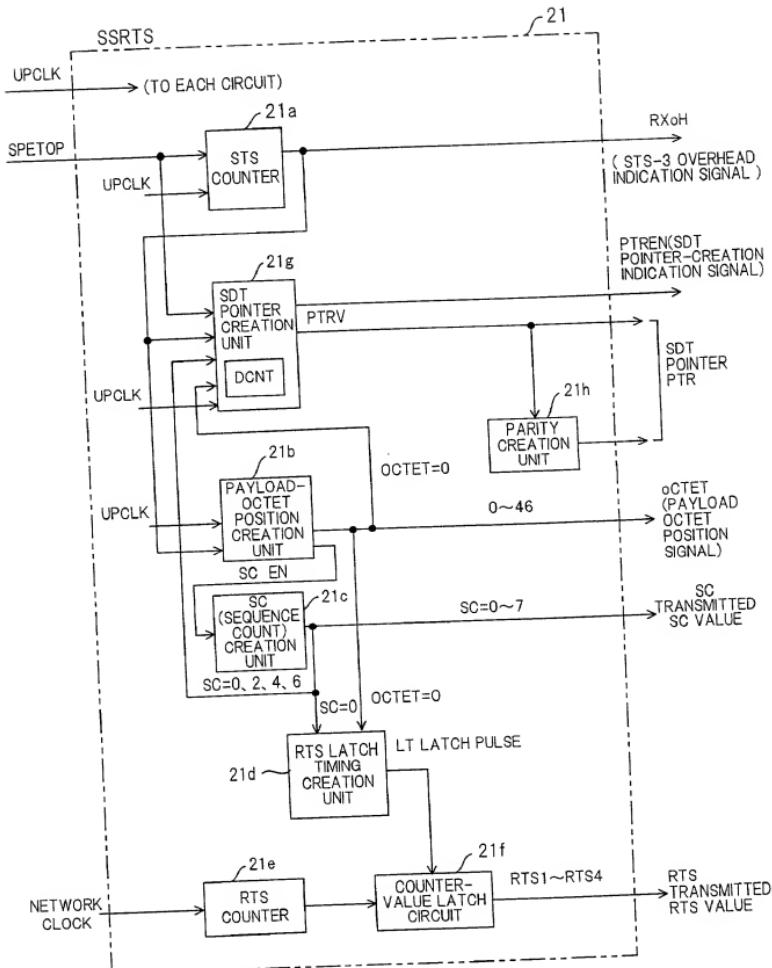


FIG. 9



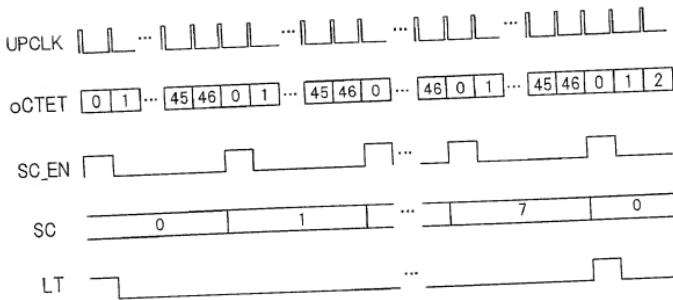
**FIG. 10**

FIG. 11

POSITION REPORTED BY TOP

\*LOWER VALUE IS VALUE OF STS-CTR COUNT  
\*\*HIGHER VALUE IS COUNT OF ROUNDS DROPPED UNDER GOALS-1 CONVERSION (RXOH = "0")

FIG. 12

POSITION REPORTED BY TOP

A1#1	A1#2	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE						
B1	X	X	E1	X	X	F1	X	X	SPE						
D1	X	X	D2	X	X	D3	X	X	SPE						
H1#1	H1#2	H1#3	H2#1	H2#2	H2#3	H3#1	H3#2	H3#3	SPE						
1574	1573	1572	1571	1570	1569	1568	1567	1566	1565	1564	1563	1562	1561	1560	1559
B2#1	B2#2	B2#3	K1	X	X	K2	X	X	SPE						
D4	X	X	D5	X	X	D6	X	X	SPE						
D7	X	X	D8	X	X	D9	X	X	SPE						
D10	X	X	D11	X	X	D12	X	X	SPE						
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	E2	X	X	SPE						
									SPE						
									2099	2098	2097				
									SPE	SPE	SPE				
									1838	1837	1836				
									SPE	SPE	SPE				
									1577	1576	1575				
									SPE	SPE	SPE				
									1307	1306	1305				
									SPE	SPE	SPE				
									1046	1045	1044				
									SPE	SPE	SPE				
									785	784	783				
									SPE	SPE	SPE				
									524	523	522				
									SPE	SPE	SPE				
									263	262	261				
									SPE	SPE	SPE				
									2	1	0				

\* LOWER VALUE IS VALUE OF DCNT COUNT  
 \*\* BYTES WITHIN BOLD BORDER UNDERGO AAI-1 CONVERSION (RX<sub>0H</sub> = "0")

## FIG. 13

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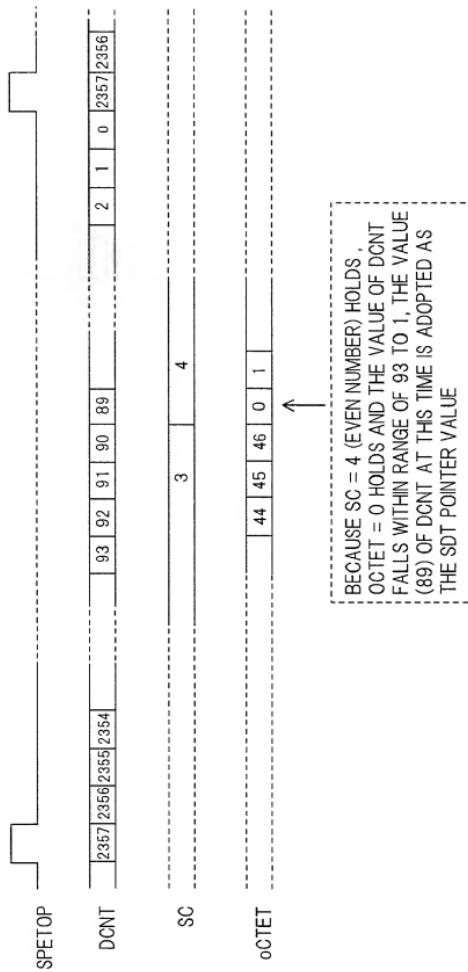


FIG. 14

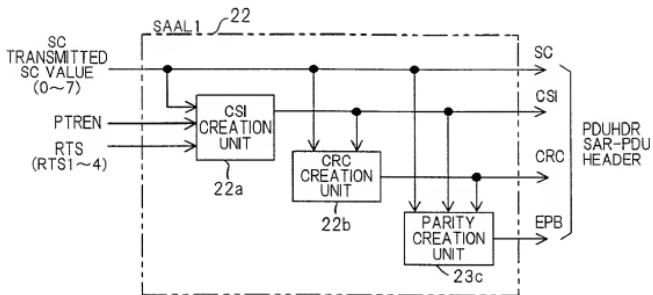
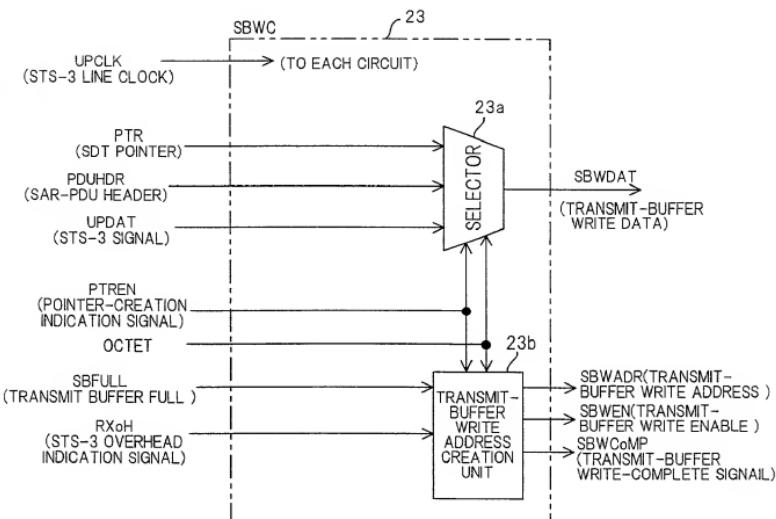
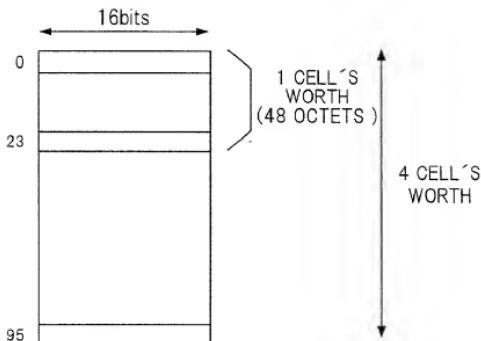


FIG. 15



**FIG. 16****FIG. 17A**

non P-format			
16 bits			
D15	D08	D07	D00
0	SAR-PDU header	1st oCTET	
1	2nd oCTET	3rd oCTET	
	4th oCTET	5th oCTET	
	6th oCTET	7th oCTET	
21	42th oCTET	43th oCTET	
22	44th oCTET	45th oCTET	
23	46th oCTET	47th oCTET	

**FIG. 17B**

P-format			
16 bits			
D15	D08	D07	D00
0	SAR-PDU header	SDT pointer	
1	1st oCTET	2nd oCTET	
2	3rd oCTET	4th oCTET	
3	5th oCTET	6th oCTET	
21	41st oCTET	42nd oCTET	
22	43rd oCTET	44th oCTET	
23	45th oCTET	46th oCTET	

FIG. 18

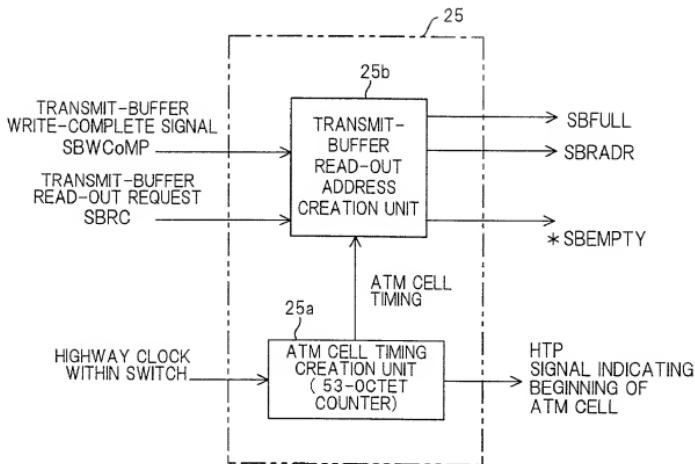
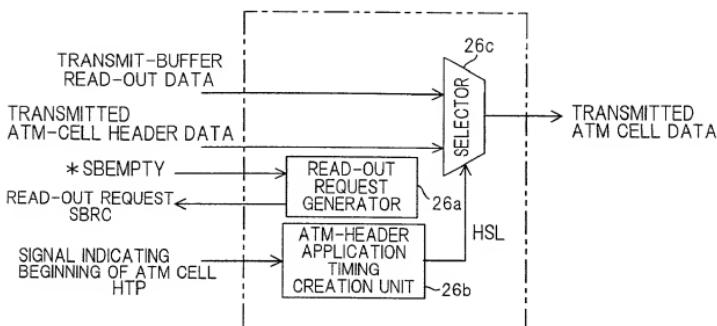
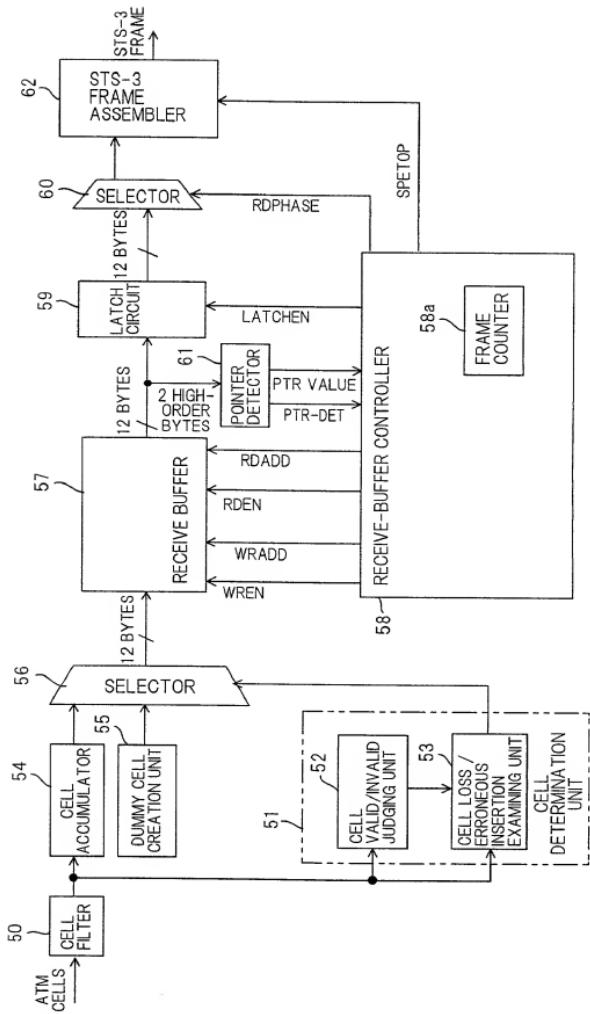


FIG. 19



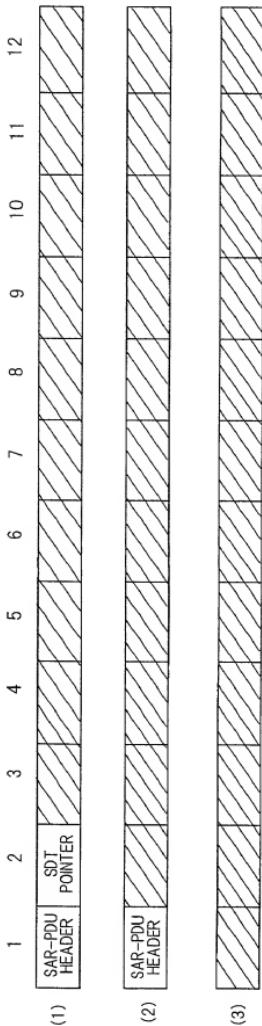
*FIG. 20*

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*FIG. 21A*

MSB	95	88	87	80	79	72	71	64	63	56	55	48	47	40	39	32	31	24	23	16	15	08	07	00	LSB
SAR-PDU HEADER	1st octet	2nd octet	3rd octet	4th octet	5th octet	6th octet	7th octet	17th octet	18th octet	19th octet	20th octet	21st octet	9th octet	10th octet	11th octet										
0	12th octet	13th octet	14th octet	15th octet	16th octet																				
1	36th octet	37th octet	38th octet	39th octet	40th octet	41st octet	42nd octet	43rd octet	44th octet	45th octet	46th octet	47th octet													
3																									

*FIG. 21B*

*FIG. 22*

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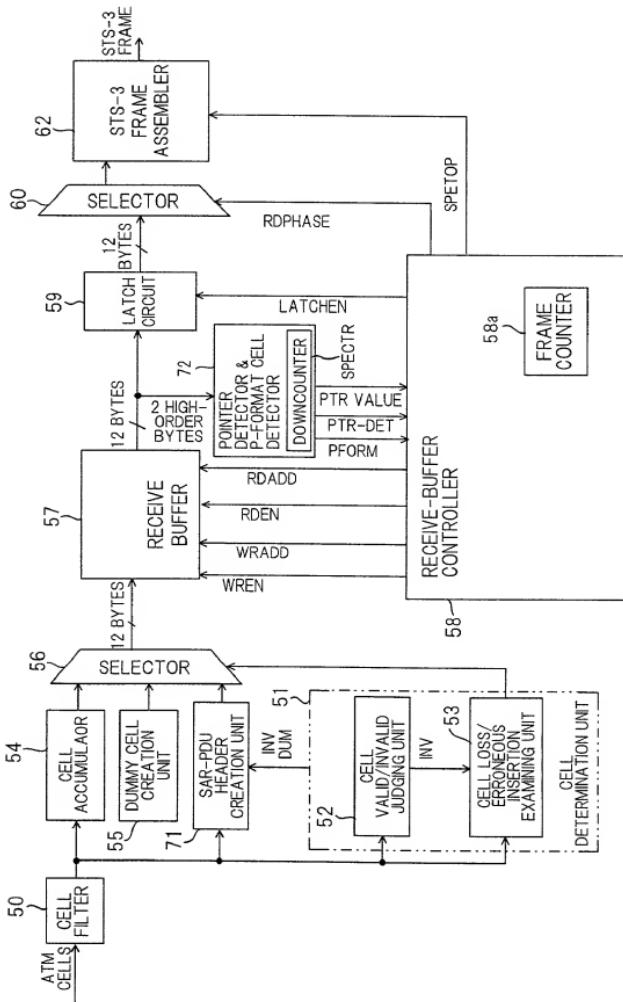
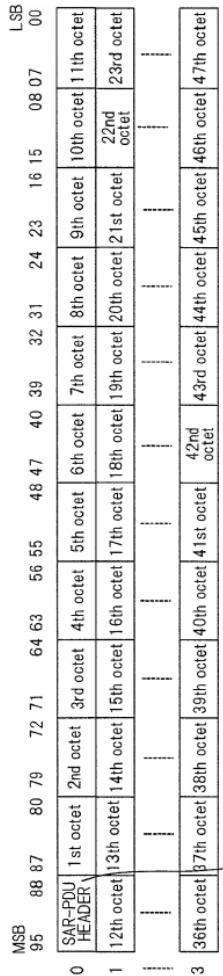


FIG. 23



CSI	MSB	'LSB'	'0'	INV	DUM	'0'
-----	-----	-------	-----	-----	-----	-----

CSI : INDICATES P-FORMAT (0: NON-P-FORMAT, 1:P-FORMAT)

SC : SEQUENCE COUNT VALUE

INV : INDICATES HEADER INVALID (0: VALID, 1: INVALID)

DUM : DUMMY CELL FLAG (0: ORDINARY CELL, 1: DUMMY CELL )

FIG. 24

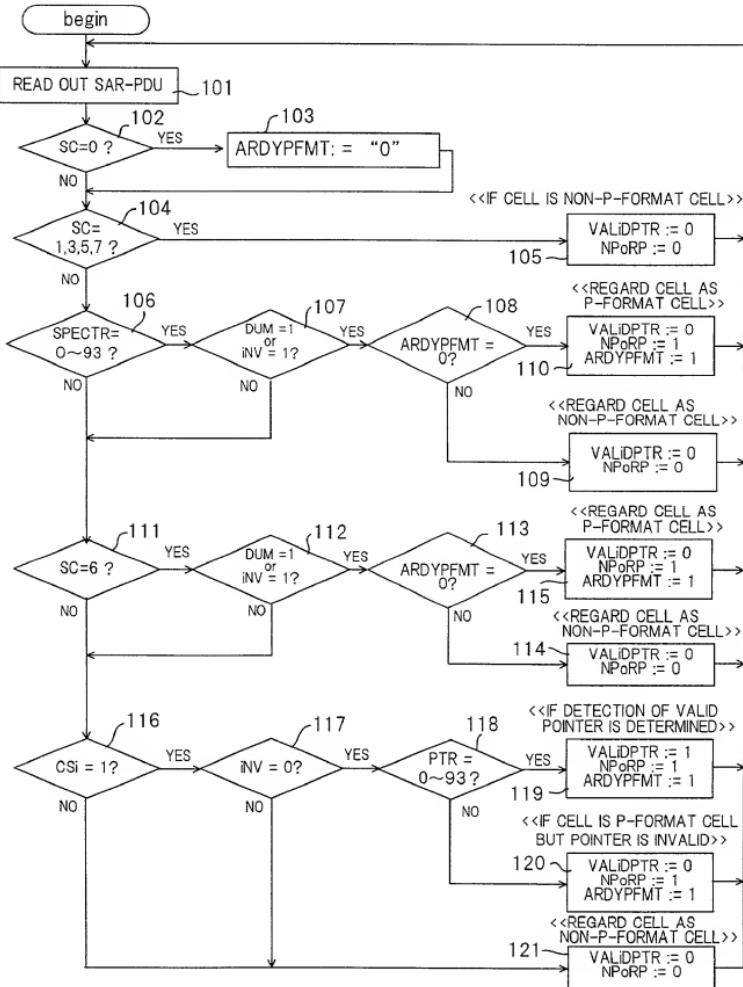


FIG. 25

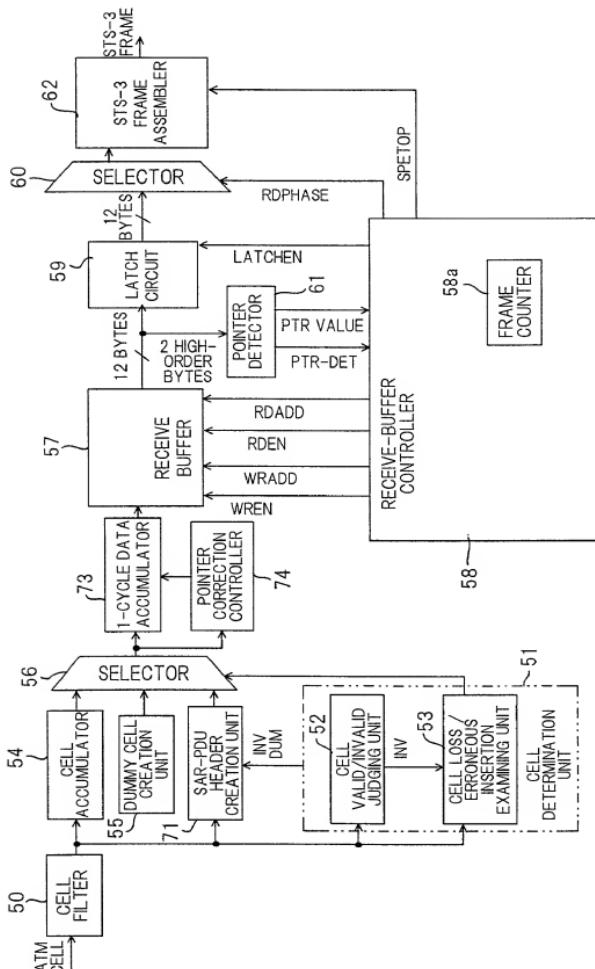


FIG. 26

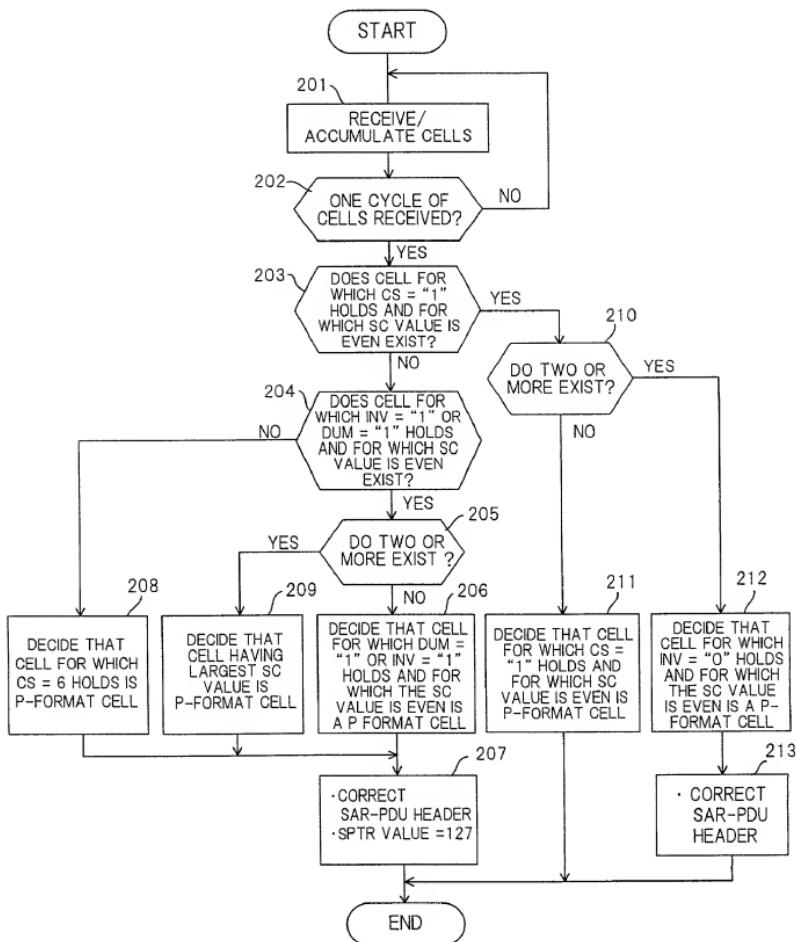


FIG. 27

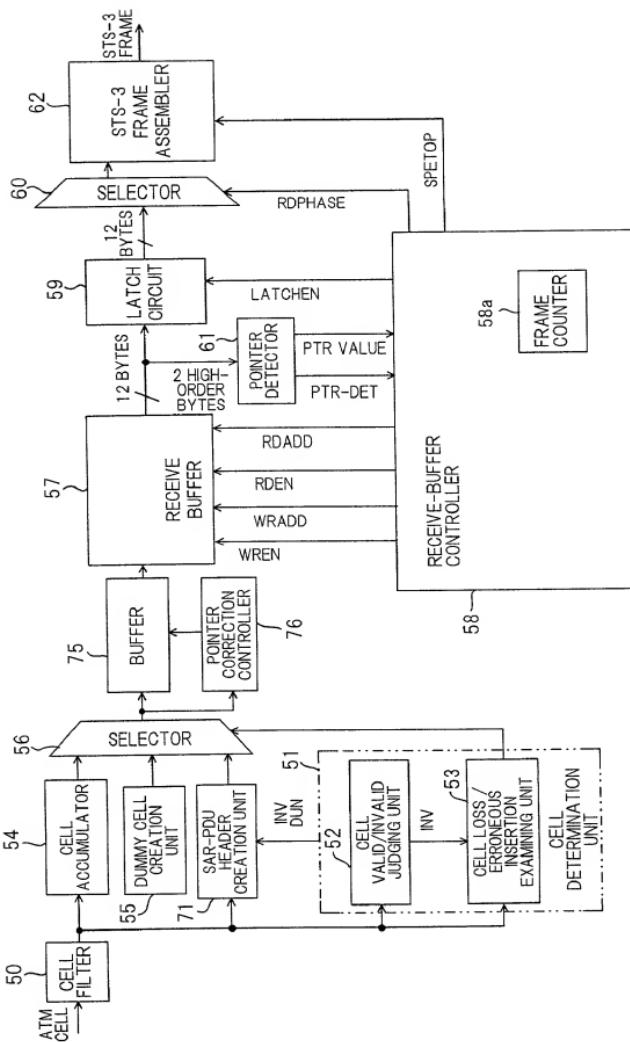


FIG. 28

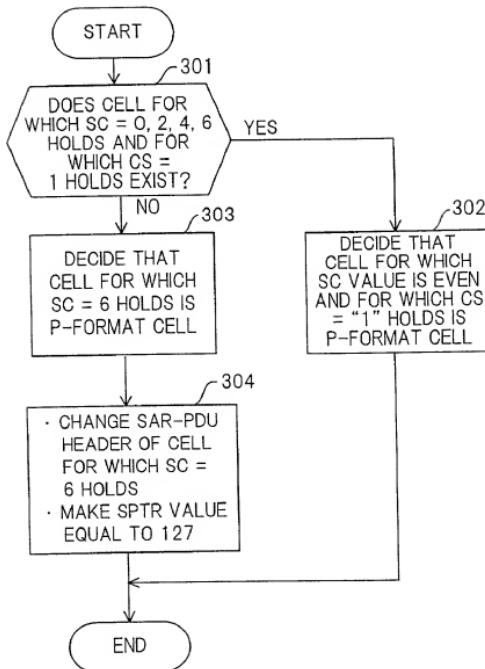


FIG. 29

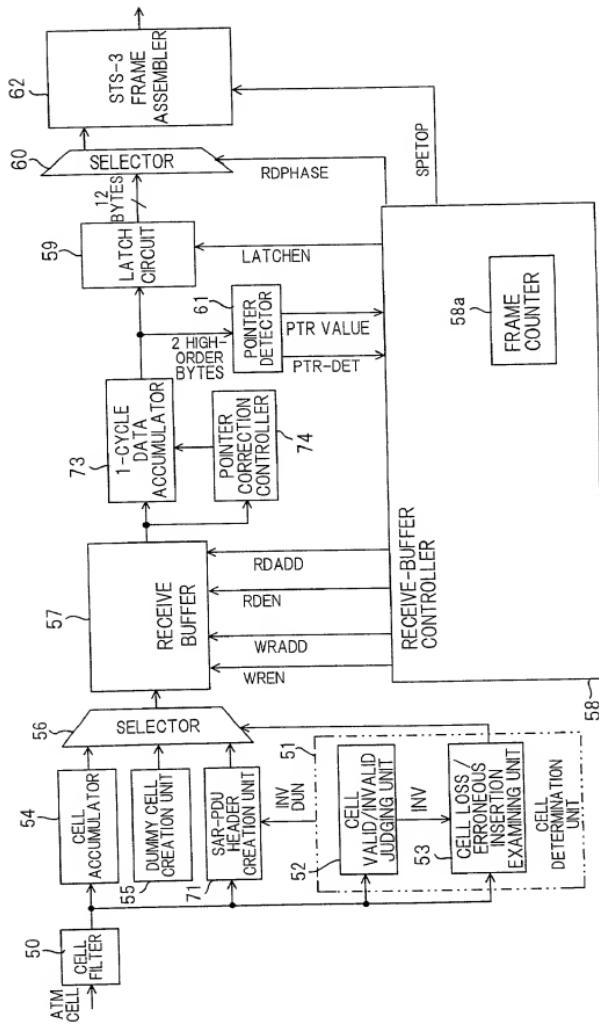


FIG. 30 PRIOR ART

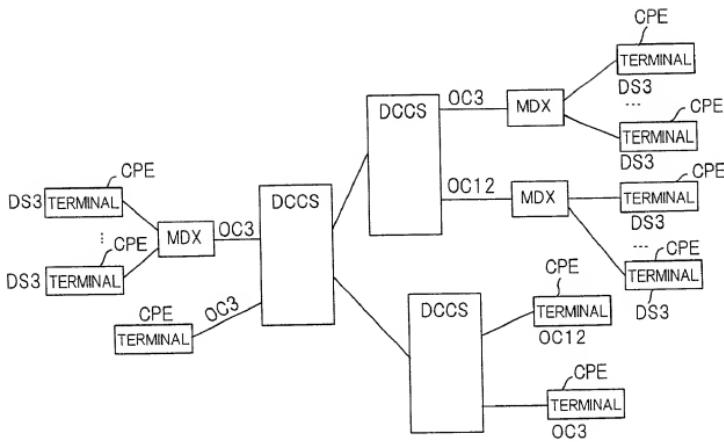


FIG. 31 PRIOR ART

